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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/226,216	01/07/1999	HISASHI OHTANI	0756-1921	1375
31780	7590 07/29/2002			
ERIC ROBINSON PMB 955 21010 SOUTHBANK ST.			EXAMINER	
			SCHILLINGER, LAURA M	
POTOMAC	FALLS, VA 20165		ART UNIT	PAPER NUMBER
			2813	,
			DATE MAILED: 07/29/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	09/226,216	OHTANI ET AL.					
Office Action Summary	Examiner	Art Unit					
	Laura M Schillinger	2813	dross				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).  - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).  Status							
1) Responsive to communication(s) filed on 23 /	<i>¶ay 2002</i> .						
2a)⊠ This action is <b>FINAL</b> . 2b)□ Th	is action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims  AN  Claim(a) 5 0 16 18 20 22 24 28 and 40 44 is/	are pending in the ann	lication					
·	Claim(s) <u>5-9,16-18,20-22,24-38 and 40-44</u> is/are pending in the application.  4a) Of the above claim(s) is/are withdrawn from consideration.						
	Claim(s) is/are allowed.						
	Claim(s) <u>5-9,16-18,20-22,24-38 and 40-44</u> is/are rejected.						
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/o	r election requirement						
Application Papers	,						
9) The specification is objected to by the Examine	r.						
10)☐ The drawing(s) filed on is/are: a)☐ acce	oted or b) objected to	by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11) ☐ The proposed drawing correction filed on is: a) ☐ approved b) ☐ disapproved by the Examiner.							
If approved, corrected drawings are required in reply to this Office action.							
12) The oath or declaration is objected to by the Examiner.							
Priority under 35 U.S.C. §§ 119 and 120							
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) 🔀 All b) 🗌 Some * c) 🔲 None of:							
	<i>1</i> -						
<del>-</del>							
<ul> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>							
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).							
a) ☐ The translation of the foreign language provisional application has been received.  15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.							
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) 🔲 Notic	view Summary (PTO-413) Paper No ce of Informal Patent Application (PT r:					

#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

- (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.
- 2. Claims 5-9 and 16-18, 20-22, 24-38, and 40-44 are rejected under 35 U.S.C. 102(e) as being anticipated by Mitanaga et al ('997).

In reference to claim 5, Mitanaga teaches a method comprising:

Forming a gate electrode and gate insulating film (Fig.1C (10, 110));

forming a semiconductor film comprising amorphous Si (Abs., lines: 1-10);

crystallizing the film by a heat treatment while a promoting material for facilitating crystallization is retained on or under the semiconductor film (Abs., lines:1-10);

removing the promoting material for crystallization form a surface of the semiconductor film after the heat treatment (Col.17, lines: 30-60);

promoting crystallinity of the crystallized semiconductor film by irradiation of laser or intense light (Col.1, lines: 55-65);

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and

wherein the promoting material comprises one or more elements selected form the groups consisting of group 14 elements (Col.3, lines: 40-50).

### Response to Arguments

Applicant's arguments filed 9/28/01 have been fully considered but they are not persuasive. Applicant argues that gettering is not removing- this argument is not persuasive because- gettering is removing. If applicant wishes to exclude gettering processes from the available methods of removing catalysts applicant MUST amend the claim language to specify removal methods which do not include gettering. Further applicant should note that the preamble is not a limitation upon a claim unless recited within the body of the claim.

In reference to claim 6, Mitanaga teaches wherein the promoting material is Ge (Col.3, lines:45-47).

In reference to claim 7, Mitanaga teaches a method comprising:

applying a solution in which a simple substance of a catalytic element for facilitating crystallization of amorphous Si film or a compound containing the catalytic element is dissolved or dispersed, on a semiconductor film comprising amorphous Si (Col.3, lines: 40-50 and Col.11, lines: 44-50);

baking the film to form a film with a catalytic element on the film (Col.11,lines: 50-60) crystallizing the amorphous Si film by carrying out a heat treatment (Col.12,lines: 43-60);

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promoting crystallinity by irradiation of laser light or intense light, wherein a plural kind of elements selected from elements in group 14 are used as the catalytic element (Col.13, lines: 25-40).

In reference to claim 8, Mitanaga teaches wherein Ge is used as the catalyst (Col.3, lines:40-50).

In reference to claim 9, Mitanaga teaches wherein the compound containing the catalytic element is at least one selected from the group consisting of GeBr (2), GECl(2), GeI(2), GeO(2), GeS(2), germane, germane acetate, tris (2,4-pentanedionate) germanium perchlorate, tetramethylgermane, tetrethylgermane, tetraphenylgermane, and hexaethyl germanium (Col.3, lines: 40-50).

In reference to claim 16, Mitanaga teaches a method comprising:

Forming a gate electrode and gate insulating film (Fig.1C (10, 110));

forming a semiconductor film comprising amorphous Si on an insulating surface (Abs., lines: 1-10);

forming a film comprising Ge in contact with the semiconductor film by VPD with a Ge gas (Col.4, lines: 25-30);

heating the film with the Ge to crystallize the film (Abs., lines: 1-10);

removing the film with Ge from a semiconductor film without changing the shape of the film after heating the film (Col.17,lines: 30-60).

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In reference to claim 17, Mitanaga teaches wherein the film comprising Ge is formed by LPCVD (Col.4, lines:25-30).

In reference to claim 18, Mitanaga teaches wherein the Ge containing gas is GeH(4) (Col.6, lines: 25-35).

In reference to claim 19, Mitanaga teaches further comprising removing the Ge containing film after crystallization (Col.17, lines: 45-60).

In reference to claim 20, Mitanaga teaches a method comprising:

forming a semiconductor film comprising amorphous Si on an insulating surface (Abs., lines: 1-10);

forming a film comprising Ge in contact with the semiconductor film by VPD with a Ge compound gas (Col.4, lines: 25-30 and Col.6, lines: 25-35);

heating the semiconductor film with the film comprising Ge to crystallize the semiconductor film (Abs., lines: 1-10);

removing the film with Ge from a semiconductor film without changing the shape of the film after heating the film (Col.17, lines: 30-60);

patterning the crystallized semiconductor film into at least one semiconductor island (Col.11, lines: 1-5);

forming a thin film transistor with the semiconductor island used as at least a channel forming region thereof (Col.11, lines: 1-10).

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In reference to claim 21, Mitanaga teaches wherein the film comprising Ge is formed by LPCVD (Col.4, lines: 25-30).

In reference to claim 22, Mitanaga teaches wherein the Ge containing gas is GeH(4) (Col.6, lines:25-35).

In reference to claim 23, Mitanaga teaches further comprising removing the film comprising Ge after the crystallization of the semiconductor film (Col.17, lines:45-60).

In reference to claim 24, Mitanaga teaches wherein the device is a video camera (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

In reference to claim 25, Mitanaga teaches wherein the device is a mobile computer (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

In reference to claim 26, Mitanaga teaches wherein the device is a portable telephone (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

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In reference to claim 27, Mitanaga teaches wherein the device is a head mount display (See

independent claim rejection above- this claim is a linking claim and is rejected as long as the

independent method claim is also rejected).

In reference to claim 28, Mitanaga teaches wherein the device is a projector (See independent

claim rejection above- this claim is a linking claim and is rejected as long as the independent

method claim is also rejected).

In reference to claim 29, Mitanaga teaches wherein the device is a video camera (See

independent claim rejection above- this claim is a linking claim and is rejected as long as the

independent method claim is also rejected).

In reference to claim 30, Mitanaga teaches wherein the device is a mobile computer (See

independent claim rejection above- this claim is a linking claim and is rejected as long as the

independent method claim is also rejected).

In reference to claim 31, Mitanaga teaches wherein the device is a portable telephone (See

independent claim rejection above- this claim is a linking claim and is rejected as long as the

independent method claim is also rejected).

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In reference to claim 32, Mitanaga teaches wherein the device is a head mount display (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

In reference to claim 33, Mitanaga teaches wherein the device is a projector (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

In reference to claim 34, Mitanaga teaches wherein the device is a video camera (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

In reference to claim 35, Mitanaga teaches wherein the device is a mobile computer (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

In reference to claim 36, Mitanaga teaches wherein the device is a portable telephone (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

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In reference to claim 37, Mitanaga teaches wherein the device is a head mount display (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

In reference to claim 38, Mitanaga teaches wherein the device is a projector (See independent claim rejection above- this claim is a linking claim and is rejected as long as the independent method claim is also rejected).

In reference to claim 40, Mitanaga teaches a method comprising:

Forming a gate electrode and gate insulating film (Fig.1C (10, 110));

forming a semiconductor film comprising amorphous Si on an insulating surface (Abs., lines: 1-10);

forming a film comprising Ge in contact with the semiconductor film by VPD with a Ge compound gas (Col.4, lines: 25-30 and Col.6, lines: 25-35);

heating the semiconductor film with the film comprising Ge to crystallize the semiconductor film (Abs., lines: 1-10);

removing the film with Ge from a semiconductor film without changing the shape of the film after heating the film (Col.17, lines: 30-60);

irradiating with laser after removing Ge (Col.15, lines: 50-55).

In reference to claim 41, Mitanaga teaches a method comprising:

Forming a gate electrode and gate insulating film (Fig.1C (10, 110));

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forming a semiconductor film comprising amorphous Si (Abs., lines: 1-10); crystallizing the film by a heat treatment while a promoting material for facilitating crystallization is retained on or under the semiconductor film (Abs., lines:1-10);

removing the promoting material for crystallization form a surface of the semiconductor film after the heat treatment (Col.17, lines: 30-60);

wherein the promoting material is of Group 14 (Col.18, lines: 55-61).

In reference to claim 42, Mitanaga teaches a method comprising:

applying a solution in which a simple substance of a catalytic element for facilitating crystallization of amorphous Si film or a compound containing the catalytic element is dissolved or dispersed, on a semiconductor film comprising amorphous Si (Col.3, lines: 40-50 and Col.11, lines: 44-50);

baking the film to form a film with a catalytic element on the film (Col.11,lines: 50-60) crystallizing the amorphous Si film by carrying out a heat treatment (Col.12,lines: 43-60); removing the film with promoting material from a semiconductor film without changing the shape of the film after heating the film (Col.17,lines: 30-60)

wherein a plural kind of elements selected from elements in group 14 are used as the catalytic element (Col.13, lines: 25-40).

In reference to claim 43, Mitanaga teaches a method comprising:

forming a semiconductor film comprising amorphous Si (Abs., lines: 1-10);

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providing a promoting material for facilitating crystallization is retained on or under the semiconductor film (Abs., lines:1-10);

crystallizing the amorphous Si film by carrying out a heat treatment (Col.12,lines: 43-60); removing the promoting material for crystallization form a surface of the semiconductor film after the heat treatment (Col.17, lines: 30-60);

promoting crystallinity by irradiation of laser light or intense light (Col.13, lines: 25-40); patterning the semiconductor film into an island (Col.15, lines: 10-15);

wherein a plural kind of elements selected from elements in group 14 are used as the catalytic element (Col.13, lines: 25-40).

In reference to claim 44, wherein removing the film with promoting material from a semiconductor film without changing the shape of the film after heating the film (Col.17,lines: 30-60).

#### Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE

MONTHS from the mailing date of this action. In the event a first reply is filed within TWO

MONTHS of the mailing date of this final action and the advisory action is not mailed until after

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the end of the THREE-MONTH shortened statutory period, then the shortened statutory period

will expire on the date the advisory action is mailed, and any extension fee pursuant to 37

CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

however, will the statutory period for reply expire later than SIX MONTHS from the mailing

date of this final action.

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Laura M Schillinger whose telephone number is (703) 308-6425.

The examiner can normally be reached on M-F 7:00 -4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Charles Bowers can be reached on (703) 308-2417. The fax phone numbers for the

organization where this application or proceeding is assigned are (703) 308-7722 for regular

communications and (703) 308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is (703) 308-1500.

LMS

July 25, 2002

OLIK CHAUDHURI

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800